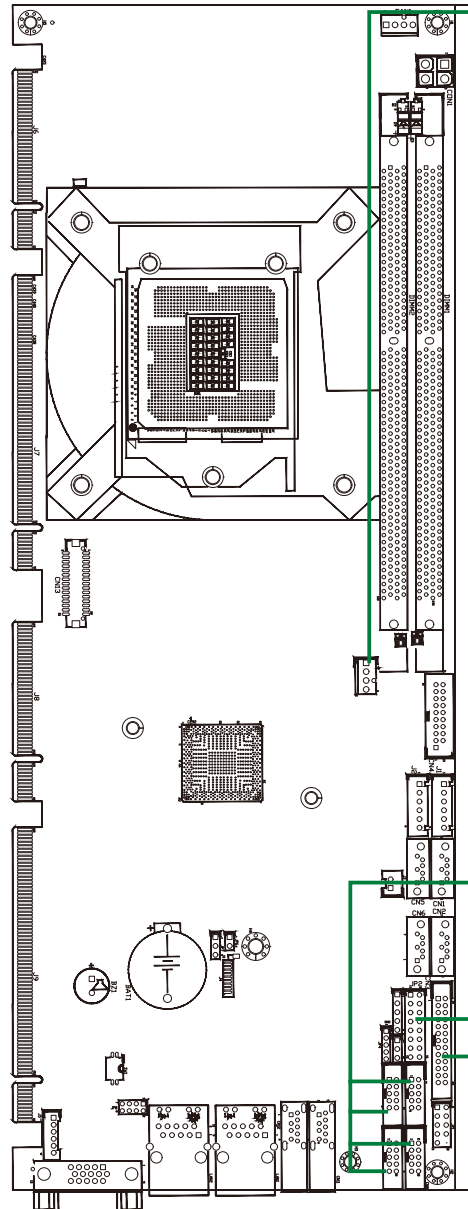


# PEAK 887 VL2 Quick Reference Guide

Ver C. (PN: 60177C0028X00)



**FAN1: CPU FAN Connector**

	Pin	Definition	Pin	Definition
	1	GND	2	+12V
	3	FAN TAC	4	FAN CTL

**FAN2: SYSTEM FAN Connector**

	Pin	Definition	Pin	Definition
	1	GND	2	+12V
	3	FAN TAC	4	FAN CTL

**CN7(COM2): Box Header Connector**

	Pin	Definition	Pin	Definition
	1	COM_DCD#2	2	COM_RXD2
	4	COM_TXD2	4	COM_DTR#2
	5	GND	6	COM_DSR#2
	7	COM_RTS#2	8	COM_CTS#2
	9	COM_RI#2_con	10	GND

**CN8(COM3): Box Header Connector**

	Pin	Definition	Pin	Definition
	1	COM_DCD#3	2	COM_RXD3
	3	COM_TXD3	4	COM_DTR#3
	5	GND	6	COM_DSR#3
	7	COM_RTS#3	8	COM_CTS#3
	9	COM_RI#3	10	GND

**CN9(COM4): Box Header Connector**

	Pin	Definition	Pin	Definition
	1	COM_DCD#4	2	COM_RXD4
	4	COM_TXD4	4	COM_DTR#4
	5	GND	6	COM_DSR#4
	7	COM_RTS#4	8	COM_CTS#4
	9	COM_RI#4	10	GND

**CN10(COM1): Box Header Connector**

	Pin	Definition	Pin	Definition
	1	COM_DCD#1	2	COM_RXD1
	3	COM_TXD1	4	COM_DTR#1
	5	GND	6	COM_DSR#1
	7	COM_RTS#1	8	COM_CTS#1
	9	COM_RI#1	10	GND

**JP2: PIN HEADER 2 x 4**

	Pin	Signal	Pin	Signal
	1	SATA_LED_P	2	PWR_LED_P
	3	SATA_LED#	4	GND
	5	GND	6	BTN_A#
	7	RST_BTN#	8	GND
	9	SPKR_OUT	10	SMB_CLK_MAIN
	11	NC	12	SMB_DATA_MAIN
	13	NC	14	S1_SYSTIN
	15	SPKR_IN	16	SAHWGND

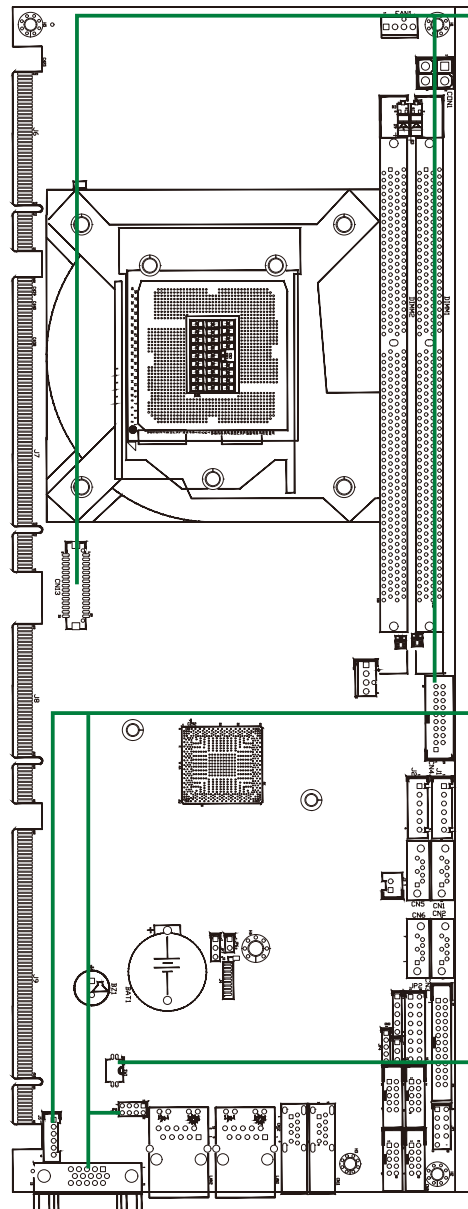
**CN3: PRINTER PORT**

	Pin	Signal	Pin	Signal
	1	RPSTB	2	RPSTB0
	3	RPSTB1	4	RPSTB2
	5	RPSTB3	6	RPSTB4
	7	RPSTB5	8	RPSTB6
	9	RPSTB7	10	S1_LPTACK#
	11	S1_LPTBUSY	12	S1_LPTPE
	13	S1_LPTSLCT	14	S1_LPTAFD#
	15	S1_LPTERR#	16	S1_LPTINIT#



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**CN13: DVI/HDMI PIN HEADER**

Pin	Definition	Pin	Definition
1	DPB_L0_P_R	2	HDMIB_PWR_R
3	DPB_L0_N_R	4	HDMIB_PWR_R
5	GND	6	DPB_PWR_R
7	DPB_L1_P_R	8	DPB_PWR_R
9	DPB_L1_N_R	10	HDMI_DIS
11	GND	12	HDMI_CTRL_CLK
13	DPB_L2_P_R	14	GND
15	DPB_L2_N_R	16	HDMI_CTRL_DATA
17	GND	18	GND
19	DPB_L3_P_R	20	GND
21	DPB_L3_N_R	22	GND
23	GND	24	HDMI_HPD
25	NC	26	GND
27	NC	28	DP_HPD
29	GND	30	GND
MH1	GND	MH2	GND

**VGA1: VGA CONNECTOR**

Pin	Signal	Pin	Signal
1	RED_VGA	2	GREEN_VGA
3	BLUE_VGA	4	NC
5	GND	6	GND
7	GND	8	GND
9	VGA_+5V	10	GND
11	NC	12	DDC_DATA_VGA
13	HSYNC_VGA	14	VSYNC_VGA
15	DDC_CLK_VGA	MH1	CHASIS_GND
MH2	CHASIS_GND		

**SW1: DIP Switch**

Pin	Signal
SW1.1	OFF: Control by SW1.2
SW1.2	ON: PCIE X1 OFF: PCIE X4

**CN4: USB3.0 BOX HEADER**

Pin	Definition	Pin	Definition
1	P5V_OC01_C	2	USB3_RX4_N_C
3	USB3_RX4_P_C	4	GND
5	USB3_TX4_N_C	6	USB3_TX4_P_C
7	GND	8	USB_1N_R
9	USB_1P_R	10	USB3_ID
11	USB_0P_R	12	USB_0N_R
13	GND	14	USB3_TX3_P_C
15	USB3_TX3_N_C	16	GND
17	USB3_RX3_P_C	18	USB3_RX3_N_C
19	P5V_OC01_C		

**J5: Keyboard and Mouse Pin header**

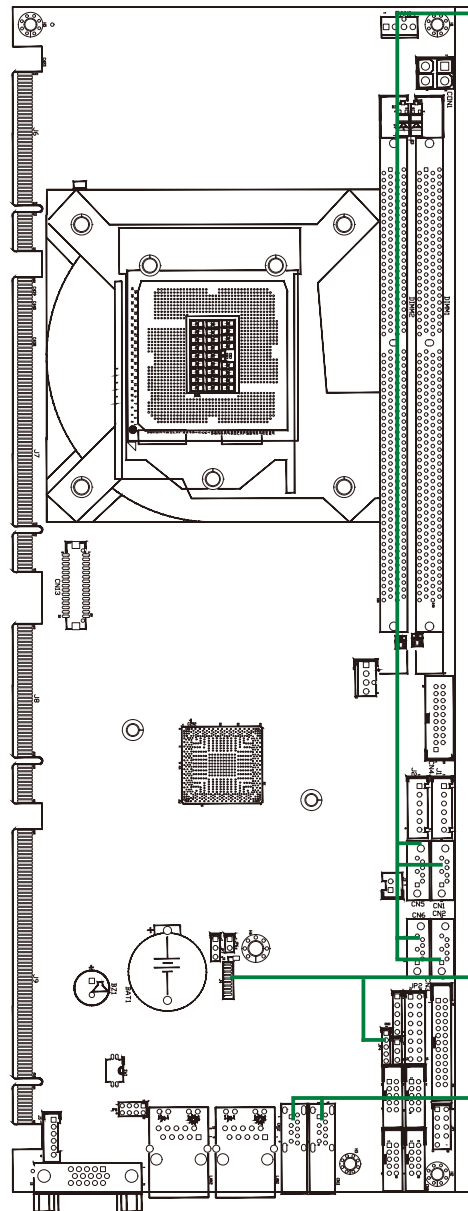
Pin	Signal	Pin	Signal
1	K/M_VCC	2	LKBDAT
3	LKBCLK	4	LMDAT
5	LMCLK	6	GND_BLK

**JP8: LAN LED PIN HEADER**

Pin	Signal	Pin	Signal
1	LAN1_LED_ACT#	2	LAN2_LED_ACT#
3	V_3P3_LAN	4	V_3P3_LAN
5	LAN1_LINK1G#	6	LAN2_LINK1G#
7	LAN1_LINK100#	8	LAN2_LINK100#

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Ver C. (PN: 60177C0028X00)



**CN1: SATA Connector**

Pin	Definition	Pin	Definition
1	GND	2	R_SATA_TXP3
3	R_SATA_TXN3	4	GND
5	B_SATA_RXN3	6	B_SATA_RXP3
7	GND		

**CN2: SATA Connector**

Pin	Definition	Pin	Definition
1	GND	2	R_SATA_TXP0
3	R_SATA_TXN0	4	GND
5	B_SATA_RXN0	6	B_SATA_RXP0
7	GND		

**CN5: SATA Connector**

Pin	Definition	Pin	Definition
1	GND	2	R_SATA_TXP2
3	R_SATA_TXN2	4	GND
5	B_SATA_RXN2	6	B_SATA_RXP2
7	GND		

**CN6: SATA Connector**

Pin	Definition	Pin	Definition
1	GND	2	R_SATA_TXP1
3	R_SATA_TXN1	4	GND
5	B_SATA_RXN1	6	B_SATA_RXP1
7	GND		

**J4 : Debug Port**

Pin	Definition	Pin	Definition
1	GND	2	RST_SIO_N
3	CLK_PCL_P80	4	LPC_FRAME#
5	LPC_AD3	6	LPC_AD2
7	LPC_AD1	8	LPC_AD0
9	VCC3	10	VCC3
MH1	GND	MH2	GND

**JP5 : COM2 RI# Voltage Select**

Pin	Definition	Pin	Definition
1	VCC5	2	COM_RI#2_con
3	+12V	4	COM_RI#2_con
5	COM_RI#2	6	

**CN12: USB3.0 Connector**

Pin	Definition	Pin	Definition
1	P5V_OC23_C	2	USB_2N_C
3	USB_2P_C	4	GND
5	USB3_RX1_N_C	6	USB3_RX1_P_C
7	GND	8	USB3_TX1_N_C
9	USB3_TX1_P_C	MH1	CHASIS_GND
MH2	CHASIS_GND	MH3	CHASIS_GND
MH4	CHASIS_GND		

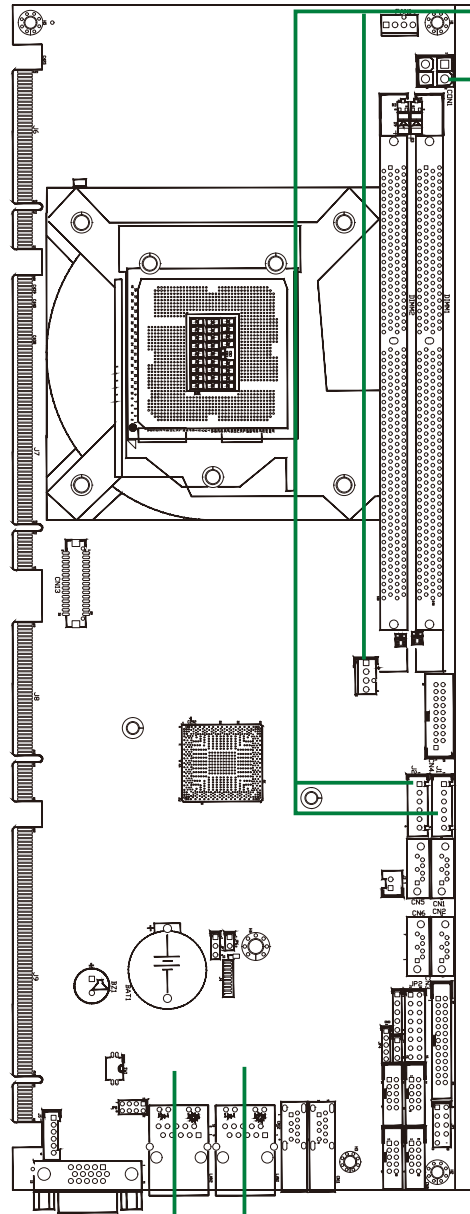
**CN11: USB3.0 Connector**

Pin	Definition	Pin	Definition
1	P5V_OC23_C	2	USB_3N_C
3	USB_3P_C	4	GND
5	USB3_RX2_N_C	6	USB3_RX2_P_C
7	GND	8	USB3_TX2_N_C
9	USB3_TX2_P_C	MH1	CHASIS_GND
MH2	CHASIS_GND	MH3	CHASIS_GND
MH4	CHASIS_GND		



# PEAK 887 VL2 Quick Reference Guide

Ver C. (PN: 60177C0028X00)



**J1: USB2.0 Connector**

	Pin	Signal	Pin	Signal
	1	P5V_OC45_C	2	USB4-
	3	USB4+	4	USB5-
	5	USB5+	6	GND

**J2: USB2.0 Connector**

	Pin	Signal	Pin	Signal
	1	P5V_OC45_C	2	USB6-
	3	USB6+	4	USB7-
	5	USB7+	6	GND

**CON1: Power input Connector**

	Pin	Signal	Pin	Signal
	1	GND	2	GND
	3	+12V	4	+12V

**J3: SATA Connector**

	Pin	Definition	Pin	Definition
	1	VCC5	2	GND

**LAN1: LAN1(I211)Connector**

	Pin	Definition	Pin	Definition	Pin	Definition	Pin	Definition
	1	LAN2_MDI0P	2	LAN2_MDI0N	3	LAN2_MDI1P	4	LAN2_MDI1N
	5	AVDD12_PHY2	6	LAN2TCTG	7	LAN2_MDI2P	8	LAN2_MDI2N
	9	LAN2_MDI3P	10	LAN2_MDI3N	11	LAN2_LINK1G#	12	LAN2_LINK100#
	13	LAN2_LED_ACT#	14	V_3P3_LAN	MH1	CHASIS_GND	MH2	CHASIS_GND

**LAN2: LAN2(I217LM)Connector**

	Pin	Definition	Pin	Definition	Pin	Definition	Pin	Definition
	1	LAN1_MDI0P	2	LAN1_MDI0N	3	LAN1_MDI1P	4	LAN1_MDI1N
	5	AVDD12_PHY1	6	LAN1TCTG	7	LAN1_MDI2P	8	LAN1_MDI2N
	9	LAN1_MDI3P	10	LAN1_MDI3N	11	LAN1_LINK1G#	12	LAN1_LINK100#
	13	LAN1_LED_ACT#	14	V_3P3_LAN	MH1	CHASIS_GND	MH2	CHASIS_GND

